# **BM1584**

# 30V 3A 500KHz Synchronous Step-Down Regulator

#### Features

- Wide 4V to 30V Operating Input Range (BM1584D输入耐压+37V)
- 3A Continuous Output Current
- Fixed 500KHz Switching Frequency
- No Schottky Diode Required
- Short Protection with Hiccup-Mode
- Built-in Over Current Limit
- Built-in Over Voltage Protection **Applications**
- CCTV Camera
- Flat-Panel Television and Monitor

- Internal Soft start
- Output Adjustable from 0.8V
- Integrated internal compensation
- Thermal Shutdown
- Available in ESOP8, SOT23 Package
- -40°C to +85°C Temperature Range
- Battery Charger
- Distributed Power Systems

### **General Description**

The BM1584 is a high frequency, synchronous, rectified, step-down, switch-mode converter with internal power MOSFETs. It offers a very compact solution to achieve a 4A peak output current over a wide input supply range, with excellent load and line regulation.

The BM1584 requires a minimal number of readily available, external components and is available in a space saving ESOP8 package.

# **Typical Application Circuit**



Figure 1. Basic Application Circuit

反馈脚对地电阻R2取值1K-15K之间

输入靠近芯片对地加电解电容与贴片电容104-226对地 C1用104,EN脚不用时用100K电阻上拉到输入端

电感选择10uH-33uH之间,输出瞬间电流越大或输出电压越高感量越大

## **Pin Description**

#### **Pin Configuration**



系列型号	输入耐压	基准电压	频率	输出电流	封装
BM1584K	30V	0.92V	500KHz	3A	ESOP8
BM1584S	30V	0.8V	600KHz	2A	S0T23-6
BM1584F1A	30V	0.8V	<b>1.3</b> MHz	<b>1.3</b> A	S0T23-6
BM1584F2	36V	0.8V	500KHz	2A	S0T23-6
BM1584D	37V	0.92V	500KHz	3A	ESOP8

BM1584 K与D脚对脚完全兼容

芯片底部的金属焊盘必须接地

#### **Pin Description**

Pin	Name	Function
1	BST	Bootstrap. A capacitor connected between SW and BST pins is required to form a floating supply across the high-side switch driver.
2	VIN	Power Supply Pin
3	SW	Switching Pin
4/9	GND/EPAD	GROUND Pin
5	FB	Adjustable Version Feedback input. Connect FB to the center point of the external resistor divider
6	NC	
7	EN	Drive this pin to a logic-high to enable the IC. Drive to a logic-low to disable the IC and enter micro-power shutdown mode.
8	NC	

BM1584K最高耐压+30V,瞬间+32Vmax;BM1584D最高耐压+37V,瞬间+40Vmax,支持环路快速 响应,支持低至1V的输入输出压差,支持轻载高效率,输入静态电流500uA左右,脚对脚兼容

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#### **Absolute Maximum Ratings**

Vin, EN, Voltage0.3V to 32V
Operating Temperature Range40°C to +85°C
FB Voltages0.3 to 6V
Lead Temperature (Soldering, 10s) +300°C
Thermal Resistance (0JA)50 °C/W
Thermal Resistance ( $\theta JC$ )10 °C/W

SW Voltage	-0.3V to (VIN+0.5V)
Storage Temperature Range	-65°C to 150°C
BS Voltage(	Vsw-0.3) to (Vsw+5V)
ESD (Machine Made) MM	200V
ESD (Human Body Made) HM	MB 2KV

Note1: Exceeding these ratings may damage the device.

Note2: The device is not guaranteed to function outside of its operating conditions.

## **Electrical Characteristics**

Parameter	Test Conditions	Min	Тур	Max	Unit
Input Voltage Range	BM1584K	4		30	V
Supply Current in Operation	VEN=3.0V, VFB=1.1V		0.4	0.6	mA
Supply Current in Shutdown	$V_{EN} = 0$ or $EN = GND$			1	μΑ
Regulated Feedback Voltage	$T_A = 25^{\circ}C, 4V \leq VIN \leq 18V$	0.784	0.8	0.816	V
High-Side Switch On-Resistance			130		m $\Omega$
Low-Side Switch On-Resistance			100		m $\Omega$
High-Side Switch Leakage Current	VEN=0V, VSW=12V			1	uA
Upper Switch Current Limit	Minimum Duty Cycle	4			А
Oscillation Frequency			0.50		MHz
Maximum Duty Cycle	V <sub>FB</sub> =0.8V			92	%
Minimum On-Time			80		nS
Minimum Off-Time			150		nS
Soft Start				1.2	mS
Thermal Shutdown			160		°C
Thermal Hysteresis			20		°C

( $V_{IN}$ =12V,  $V_{OUT}$ =5V,  $T_A$  = 25°C, unless otherwise noted.)

Note1: 100% production test at +25°C. Specifications over the temperature range are guaranteed by design and characterization.

Note2: Dynamic supply current is higher due to the gate charge being delivered at the switching frequency.

# **Functional Application Circuit**



Figure 2. BM1584 K 或 BM1584 D Application Circuit

# Function Description

#### **Internal Regulator**

The BM1584 is a current mode step down DC/DC converter that provides excellent transient response with no extra external compensation components. This device contains an internal, low resistance, high voltage power MOSFET, and operates at a high 500K operating frequency to ensure a compact, high efficiency design with excellent AC and DC performance.

#### **Error Amplifier**

The error amplifier compares the FB pin voltage with the internal FB reference (VFB) and outputs a current proportional to the difference between the two. This output current is then used to charge or discharge the internal compensation network to form the COMP voltage, which is used to control the power MOSFET current. The optimized internal compensation network minimizes the external component counts and simplifies the control loop design.

#### **Internal Soft-Start**

The soft-start is implemented to prevent the converter output voltage from overshooting during startup. When the chip starts, the internal circuitry generates a soft-start voltage (SS) ramping up from 0V to 0.8V. When it is lower than the internal reference (REF), SS overrides REF so the error amplifier uses SS as the reference. When SS is higher than REF, REF regains control. The SS time is internally fixed to 1.2ms.

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#### **Over Current Protection & Hiccup**

The BM1584 has cycle-by-cycle over current limit when the inductor current peak value exceeds the set current limit threshold. Meanwhile, output voltage starts to drop until FB is below the Under-Voltage (UV) threshold, typically 25% below the reference. Once a UV is triggered, the BM1584 enters hiccup mode to periodically restart the part. This protection mode is especially useful when the output is dead-short to ground. The average short circuit current is greatly reduced to alleviate the thermal issue and to protect the regulator. The BM1584 exits the hiccup mode once the over current condition is removed.

#### Startup and Shutdown

If both VIN and EN are higher than their appropriate thresholds, the chip starts. The reference block starts first, generating stable reference voltage and currents, and then the internal regulator is enabled. The regulator provides stable supply for the remaining circuitries. Three events can shut down the chip: EN low, VIN low and thermal shutdown. In the shutdown procedure, the signaling path is first blocked to avoid any fault triggering. The COMP voltage and the internal supply rail are then pulled down. The floating driver is not subject to this shutdown command.

#### **Applications Information**

#### Setting the Output Voltage

BM1584 require an input capacitor, an output capacitor and an inductor. These components are critical to the performance of the device. BM1584 are internally compensated and do not require external components to achieve stable operation. The output voltage can be programmed by resistor divider.

$$V_{OUT} = V_{FEEDBACK} \times \frac{R1 + R2}{R2}$$

V <sub>OUT</sub>	R1	R2对地电阻	L1 <sub>MIN</sub>	典型电感值	L1 <sub>MAX</sub>	C <sub>IN</sub>	C <sub>OUT</sub>
1V	1.25KΩ	5ΚΩ	4.7µH		10µH	20-47uF	20-68uF
1.05V	1.562KΩ	5ΚΩ	4.7μΗ		10µH	20-47uF	20-68uF
1.2V	2.5KΩ	5ΚΩ	4.7µH		10µH	20-47uF	20-68uF
1.5V	4.375KΩ	5ΚΩ	4.7μΗ		10µH	20-47uF	20-68uF
3.3V	15.625KΩ	5ΚΩ	6.8µH		22µН	20-47uF	20-68uF
5.0V	26.25KΩ	5ΚΩ	10µH		22µH	20-47uF	20-68uF

表格是 BM1584S and BM1584K 在0.8V基准,在输入无过冲条件下推荐参数,如可能有过冲,输入加大电解电容

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#### Selecting the Inductor

The recommended inductor values are shown in the Application Diagram. It is important to guarantee the inductor core does not saturate during any foreseeable operational situation. The inductor should be rated to handle the peak load current plus the ripple current: Care should be taken when reviewing the different saturation current ratings that are specified by different manufacturers. Saturation current ratings are typically specified at 25°C, so ratings at maximum ambient temperature of the application should be requested from the manufacturer.

$$L = \frac{V_{out} \times (V_{in} - V_{out})}{V_{in} \times \Delta I_L \times f_{OSC}}$$

Where  $\Delta$ IL is the inductor ripple current. Choose inductor ripple current to be approximately 30% if the maximum load current, 3A. The maximum inductor peak current is:

$$I_{L(MAX)} = I_{LOAD} + \frac{\Delta I_L}{2}$$

Under light load conditions below 100mA, larger inductance is recommended for improved efficiency.

#### Selecting the Output Capacitor

Special attention should be paid when selecting these components. The DC bias of these capacitors can result in a capacitance value that falls below the minimum value given in the recommended capacitor specifications table.

The ceramic capacitor's actual capacitance can vary with temperature. The capacitor type X7R, which operates over a temperature range of  $-55^{\circ}$ C to  $+125^{\circ}$ C, will only vary the capacitance to within  $\pm 15\%$ . The capacitor type X5R has a similar tolerance over a reduced temperature range of  $-55^{\circ}$ C to  $+85^{\circ}$ C. Many large value ceramic capacitors, larger than 1uF are manufactured with Z5U or Y5V temperature characteristics. Their capacitance can drop by more than 50% as the temperature varies from 25°C to 85°C. Therefore X5R or X7R is recommended over Z5U and Y5V in applications where the ambient temperature will change significantly above or below 25°C.

Tantalum capacitors are less desirable than ceramic for use as output capacitors because they are more expensive when comparing equivalent capacitance and voltage ratings in the 0.47uF to 44uF range. Another important consideration is that tantalum capacitors have higher ESR values than equivalent size ceramics. This means that while it may be possible to find a tantalum capacitor with an ESR value within the stable range, it would have to be larger in capacitance (which means bigger and more costly) than a ceramic capacitor with the same ESR value. It should also be noted that the ESR of a typical tantalum will increase about 2:1 as the temperature goes from  $25^{\circ}$ C down to  $-40^{\circ}$ C, so some guard band must be allowed.

#### PC Board Layout Consideration

PCB layout is very important to achieve stable operation. It is highly recommended to duplicate EVB layout for optimum performance. If change is necessary, please follow these guidelines and take Figure 4 for reference.

- 1. Keep the path of switching current short and minimize the loop area formed by Input capacitor, high-side MOSFET and low-side MOSFET.
- 2. Bypass ceramic capacitors are suggested to be put close to the Vin Pin.
- 3. Ensure all feedback connections are short and direct. Place the feedback resistors and compensation components as close to the chip as possible.
- 4. VOUT, SW away from sensitive analog areas such as FB.
- 5. Connect IN, SW, and especially GND respectively to a large copper area to cool the chip to improve thermal performance and long-term reliability.

## Package Description

#### ESOP8 (EXPOSED PAD)



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# Package Description



